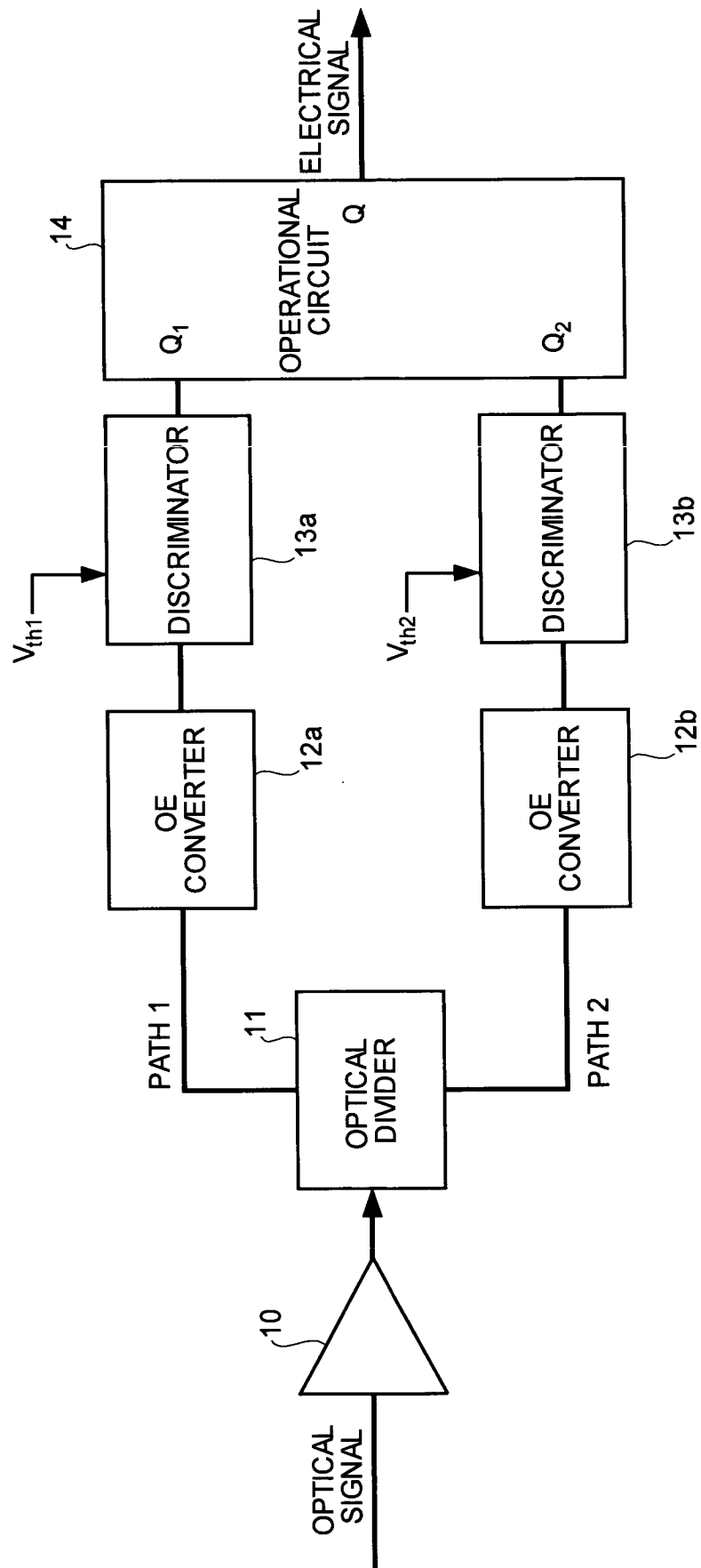


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FIG.1



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FIG.2A

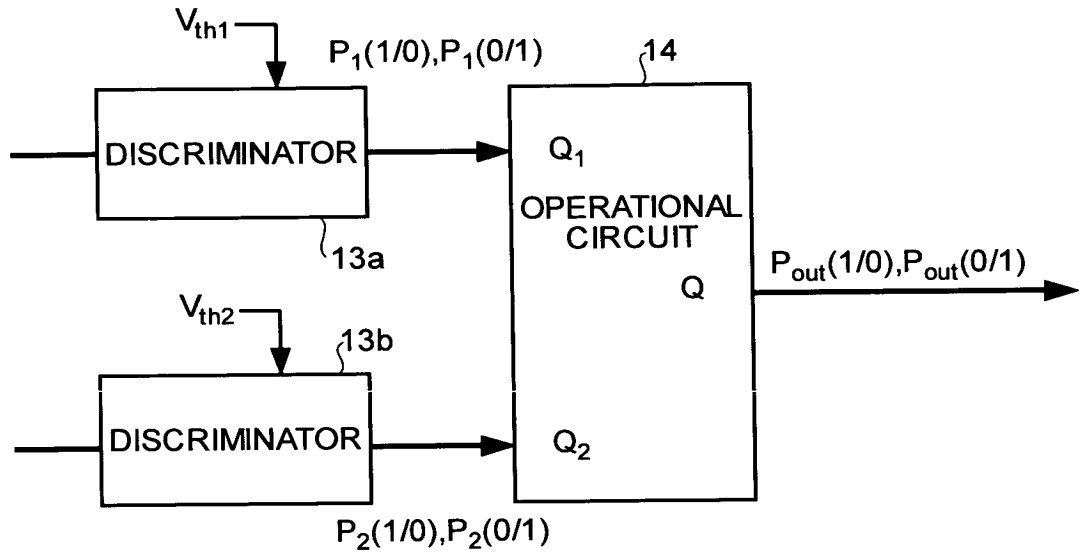


FIG.2B

TRUTH TABLE OF OR CIRCUIT

Q1	Q2	Q
0	0	0
1	0	1
0	1	1
1	1	1

FIG.2C

TRUTH TABLE OF AND CIRCUIT

Q1	Q2	Q
0	0	0
1	0	0
0	1	0
1	1	1

FIG.3

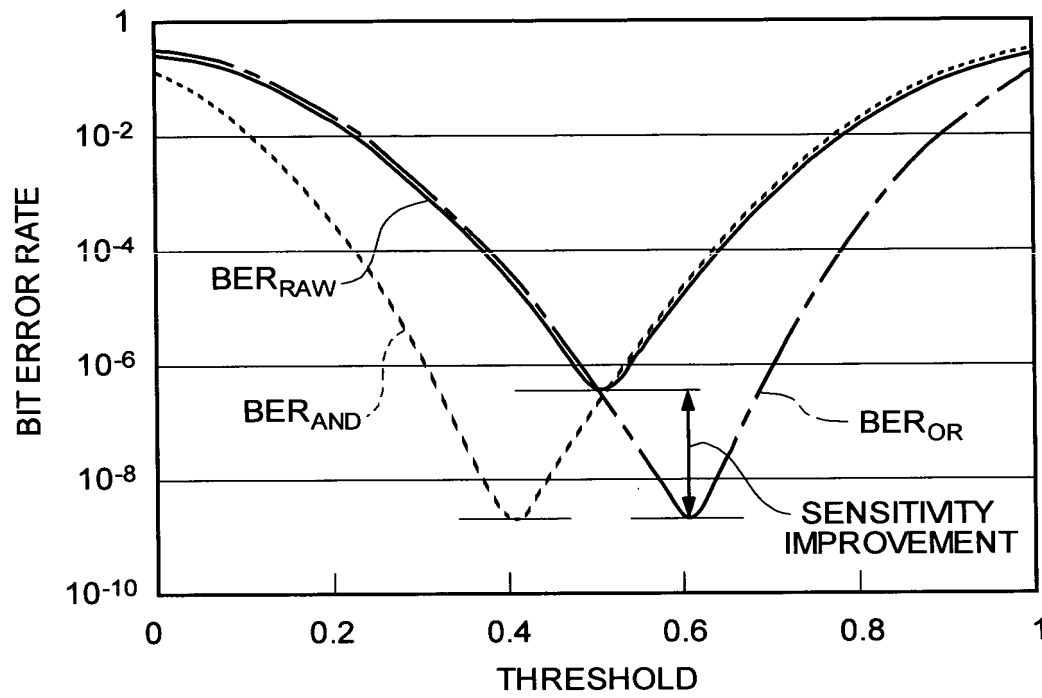
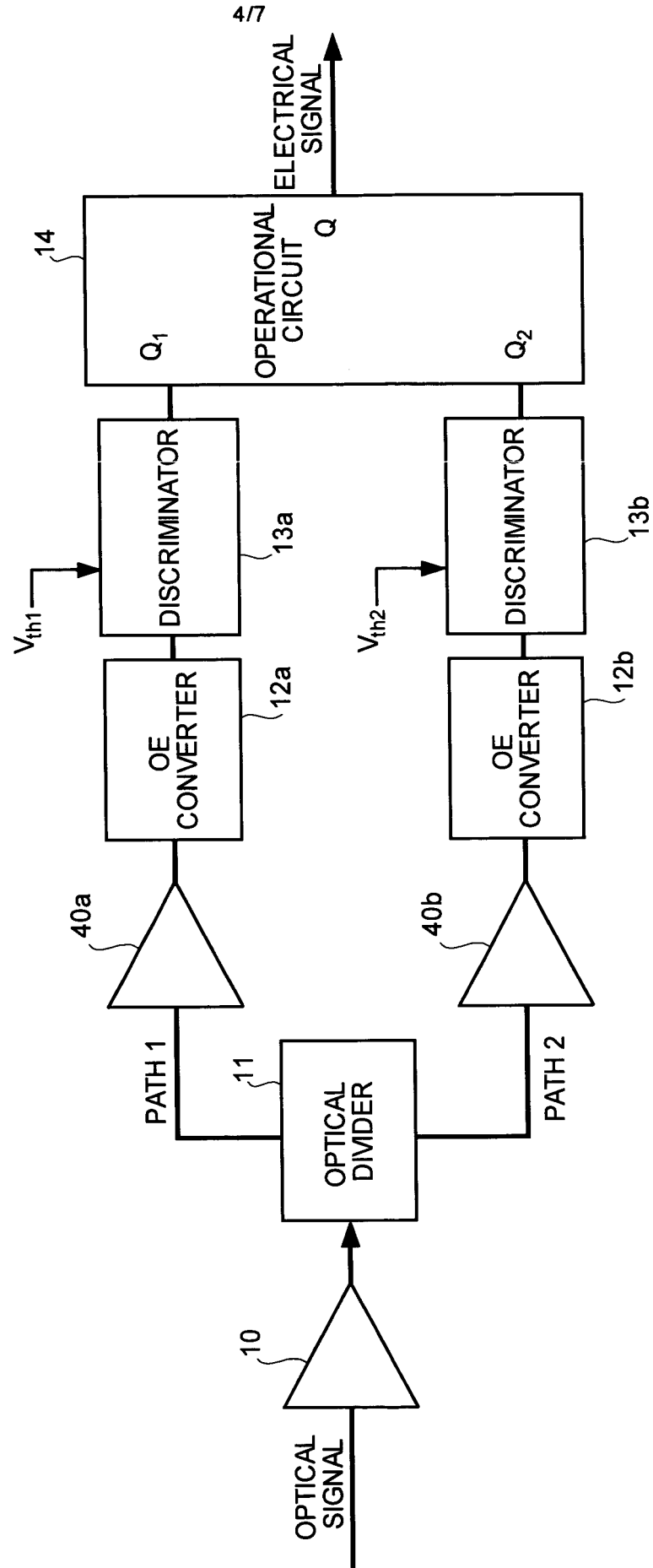
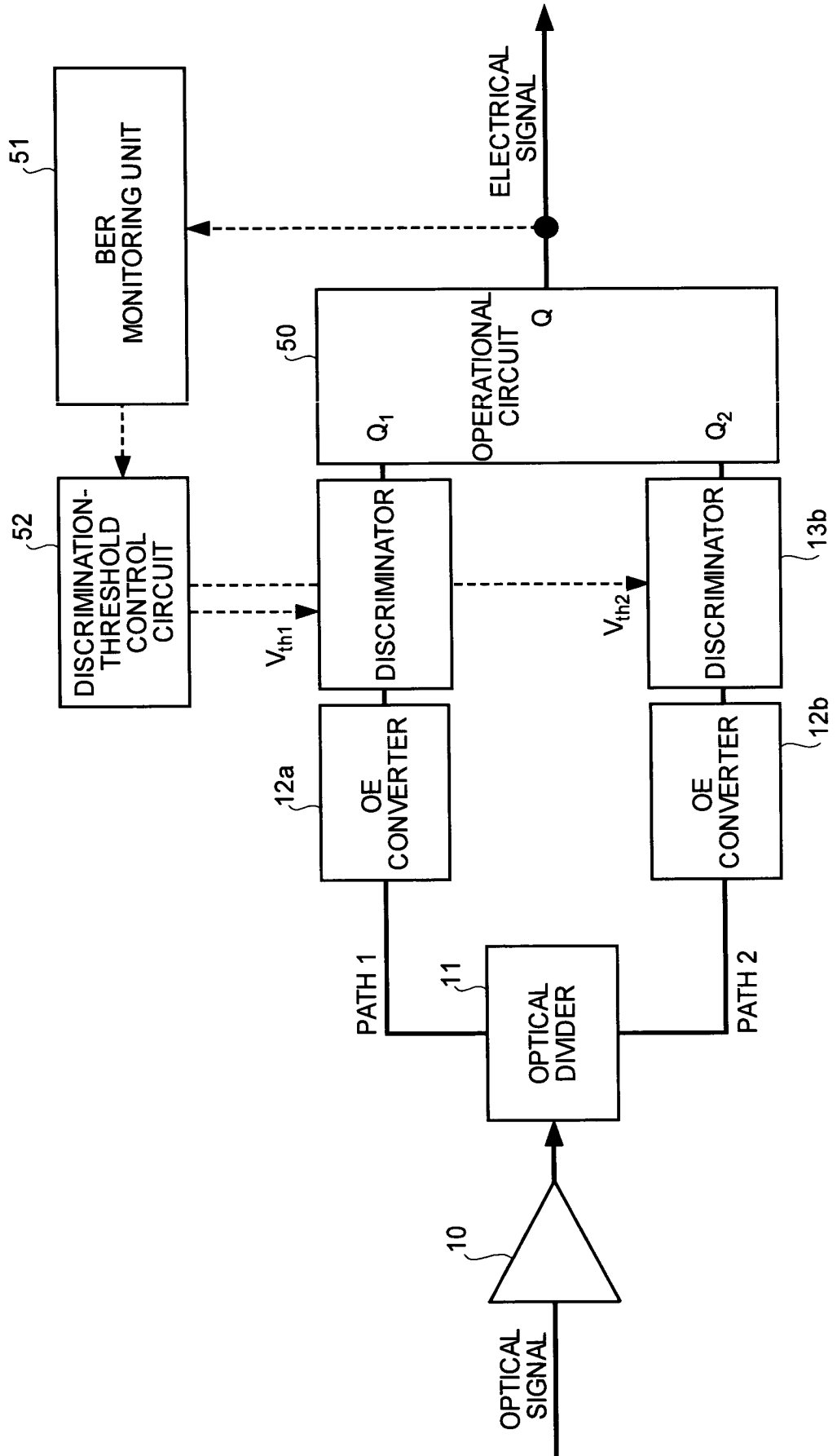


FIG.4



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FIG.5



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FIG. 6

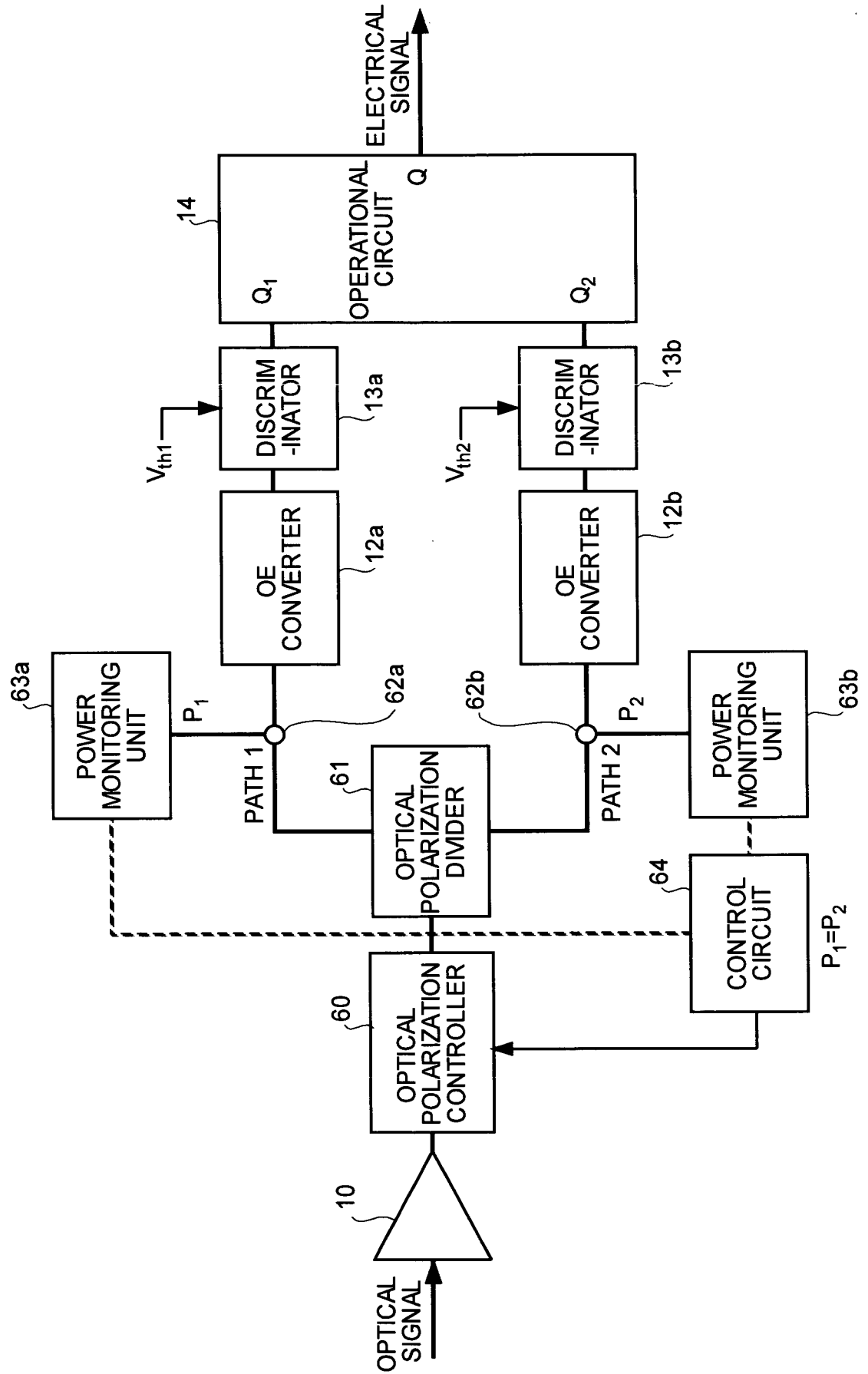


FIG. 7

